## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Mukunda Krishnappa et al.

Filed: December 15, 2003

For: Apparatus and Methods for Configuration of Programmable Logic Devices

Serial No.: 10/736,167

Group Art Unit: 2819

Examiner: Chang, D.

Att'y Docket No.: ALTR:024

Pursuant to 37 C.F.R. § 1.8, I certify that this correspondence is being filed electronically, via facsimile, or deposited with the U.S. Postal Service in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date below:

MARA

February 2, 2007

Date Name

C D

Commissioner for Patents

Alexandria 22313-1450

P.O. Box 1450

## **AMENDMENT & RESPONSE TO OFFICE ACTION**

Applicant submits this paper in response to an office action dated October 4, 2006 ("Office Action"). Concurrently, the Applicant files a request for an extension of time. The Applicant requests entry and consideration of the papers.